

EAST Search History**EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6656	(circuit same (design diagram simulat\$5 model \$3)) and (damping same resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:36
L2	3346	(circuit same (design diagram simulat\$5 model \$3)) and (damping with resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:36
L3	2700	(circuit with (design diagram simulat\$5 model \$3)) and (damping with resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:36
L4	2575	(circuit with (design diagram)) and (damping with resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:37
L5	722	(circuit with (design diagram)) and (damping adj resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:37
L6	429	(circuit adj (design diagram)) and (damping adj resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:37

L7	28	(circuit adj (design diagram)) and (damping adj resistance) and (bypass adj capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:37
L9	43	(circuit with (design diagram) with analy\$4) and (damping with resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:38
L10	164	(circuit with (design diagram) with analy\$4) and (bypass with capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:39
L11	140	(circuit with (design diagram) with analy\$4) and (bypass adj capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:39
L12	150	(circuit with (design diagram) with analy\$4) and (bypass adj capacit\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/07/01 21:39

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L8	15	(circuit adj (design diagram)) and (damping adj resistance) and (bypass adj capacitor)	USPAT; UPAD	OR	ON	2010/07/01 21:38
L13	92	(circuit with (design diagram) with analy\$4) and (bypass adj capacit\$4)	USPAT; UPAD	OR	ON	2010/07/01 21:39

L14	2269	(circuit same (design diagram simulat\$5 model \$3)) and (damping same resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:39
L15	1470	(circuit same (design diagram simulat\$5 model \$3)) and (damping with resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L16	1284	(circuit with (design diagram simulat\$5 model \$3)) and (damping with resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L17	1220	(circuit with (design diagram)) and (damping with resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L18	323	(circuit with (design diagram)) and (damping adj resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L19	206	(circuit adj (design diagram)) and (damping adj resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L20	15	(circuit adj (design diagram)) and (damping adj resistance) and (bypass adj capacitor)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L21	19	(circuit with (design diagram) with analy\$4) and (damping with resistance)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L22	103	(circuit with (design diagram) with analy\$4) and (bypass with capacitor)	USPAT; UPAD	OR	ON	2010/07/01 21:40

L23	86	(circuit with (design diagram) with analy\$4) and (bypass adj capacitor)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L24	92	(circuit with (design diagram) with analy\$4) and (bypass adj capacit\$4)	USPAT; UPAD	OR	ON	2010/07/01 21:40
L25	45	(circuit same (design diagram simulat\$5 model \$3)) and (damping same resistance) and (database keyword)	USPAT; UPAD	OR	ON	2010/07/01 21:42
L26	5154	703/13 703/14 703/15	USPAT; UPAD	OR	ON	2010/07/01 21:42
L27	9	I14 and I26	USPAT; UPAD	OR	ON	2010/07/01 21:42
L28	9656	(circuit same (design diagram simulat\$5 model \$3)) and (bypass same capacit\$5)	USPAT; UPAD	OR	ON	2010/07/01 21:43
L29	4524	(circuit same (design diagram simulat\$5 model \$3)) and (bypass adj capacit\$5)	USPAT; UPAD	OR	ON	2010/07/01 21:43
L30	86	(circuit same (design diagram simulat\$5 model \$3)) and (bypass adj capacit\$5) and (database keyword)	USPAT; UPAD	OR	ON	2010/07/01 21:43
L31	84	(circuit same (design diagram)) and (bypass adj capacit\$5) and (database keyword)	USPAT; UPAD	OR	ON	2010/07/01 21:43
L32	5	I26 and I31	USPAT; UPAD	OR	ON	2010/07/01 21:44

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